



# New Project Proposal: Reopen SFF-TA-1009 (EDSFF Pin and Signal Specification)

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# New Project Proposal: SFF-TA-1009 R4.1

- Reopen specification for PCIe 7.0 support. Address several editorial and clarifications that have come up in the past year.
- Editor(s): Anthony Constantine
- Supporters
  1. AMD
  2. Dell
  3. Kioxia
  4. Micron
  5. Microsoft
  6. Samsung
  7. Sandisk
  8. Solidigm

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- **Changes proposed:**
  - Boilerplate stuff (fix typos, add E2).
  - Dual port alignment to PCI-SIG
  - Address REFCLK feedback
  - Address SMBus/I3C feedback
  - Inrush clarification
  - Forced recovery timings?
  - LED table conflicts, alignment to JEDEC, other concerns.
  - Add PCIe Electrical requirements for PCIe 7.0
  - I3C flow fixes and clarifications.
  - Additional proposals if needed.
- **IP Declaration (if applicable):**
  - No known new IP with these changes
- **General timeline for project completion**
  - 1<sup>st</sup> draft sometime in April/May.
  - Approval ballot before November.



# Thank You

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