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Update Project Proposal: SFF-9400 Universal 4/8X Pinouts

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Supporters: Jason Stuhlsatz [Broadcom], Paul Kaler [HPE]

Update Project Proposal: SFF-9400 R1.0 Universal 4/8X Pinouts

- Goal is to make fixes based on external feedback received showing errors in this reference document.
- Editor(s): Tentatively Anthony Constantine and Jason Stuhlsatz
- Supporters:
 1. Broadcom
 2. Micron
 3. HPE

Update Project Proposal: SFF-9400 R1.0 Universal 4/8X Pinouts

- **What's in scope:**

- Correct errors in wording for Type 2 4X and 8X pinouts.
- Fix references, boilerplate, and bookmarks
- Other editorial as needed.
- Not adding new content.

- **IP Declaration (if applicable):**

- None

- **Optional: General timeline for project completion**

- Draft and ballot in the next few weeks.

External comment on SFF-9400

3.2 Type 2 4X Pinout

The Type 2 4X Pinout assigns seven vendor specific pins as discrete sideband signals and three for an additional high-speed differential pair as shown in Figure 3-4.

Discrepancy between statement and figure 3-4 in spec PUBLISHED SFF-9400.

3.2 Type 2 4X Pinout

The Type 2 4X Pinout assigns seven vendor specific pins as discrete sideband signals and three for an additional high-speed differential pair as shown in Figure 3-4.

R	G	H	H	G	H	H	G	VENDOR SPECIFIC	G	H	H	G	H	H	G	R
S	N	S	S	N	S	S	N		G	S	S	N	S	S	N	S
R	G	H	H	G	H	H	G	VENDOR SPECIFIC	G	H	H	G	H	H	G	R
S	N	S	S	N	S	S	N		G	S	S	N	S	S	N	S

FIGURE 3-1: LOCATION OF VENDOR SPECIFIC PINS IN A 4X PINOUT

R	G	H	H	G	H	H	G	H	H	G	R	G	H	H	G	R
S	N	S	S	N	S	S	N	S	S	N	S	S	S	S	N	V
R	G	H	H	G	H	H	G	H	H	G	R	G	H	H	G	R
S	N	S	S	N	S	S	N	S	S	N	S	S	S	S	N	V

FIGURE 3-4: TYPE 2 4X PINOUT

- This looks like an error in the wording.
- Maybe it should say: "...four vendor specific pins as discrete sideband signals, four for an additional high-speed differential pair, and two ground pins..."



Thank You

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