

## Request to Reopen SFF-9402

Add PCI-SIG CopprLink and OCP M-XIO
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## Overview

- Intent is to add new connector pinouts to the SFF-9402 cable tables
- OCP has defined a PCIe pinout for the SFF-TA-1016 connector (named M-XIO)
- Likewise, PCI-SIG is adding a PCIe pinout for the SFF-TA-1016 (named Internal CopprLink)
- Both are attempting to match each other
- SFF-TA-1016 matches the same pin count and arrangement that SFF-8654 has in SFF-9402 tables, pin assignments are very similar
- Limited to primary SFF-9402 intent to show cabling to backplanes

