



SFF-TA-1036: Cable Optimized Boot Peripheral Connector

Presented: 03/31/2023

Editor: Egide Murisa [Molex]

Supporters: Anthony Constantine [Intel]

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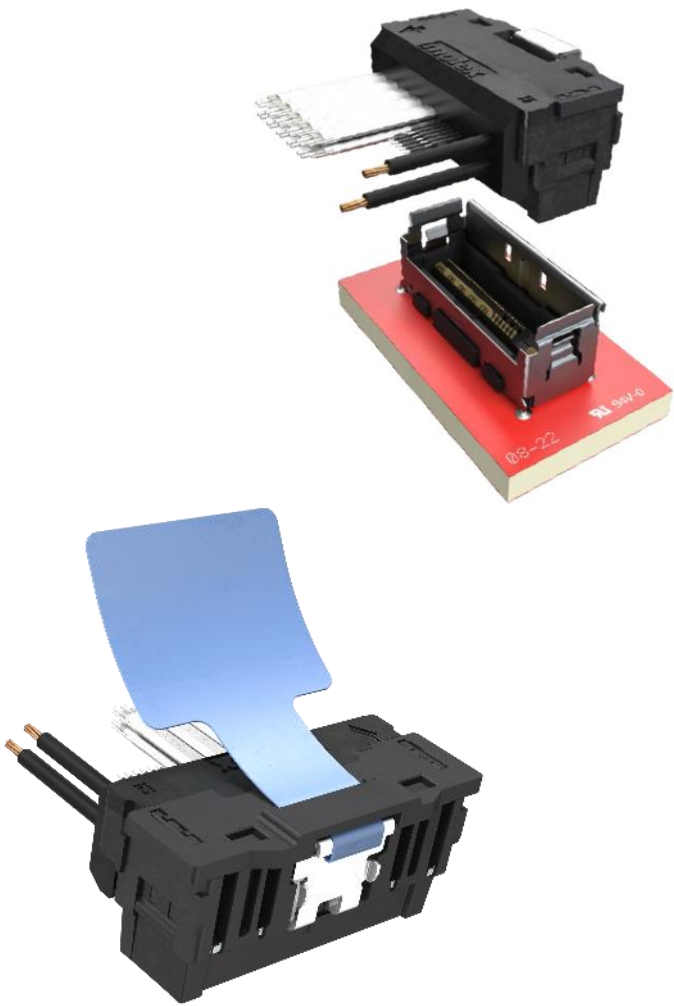
Michael Gregoire [Dell]

New Project Proposal: SFF-TA-1036

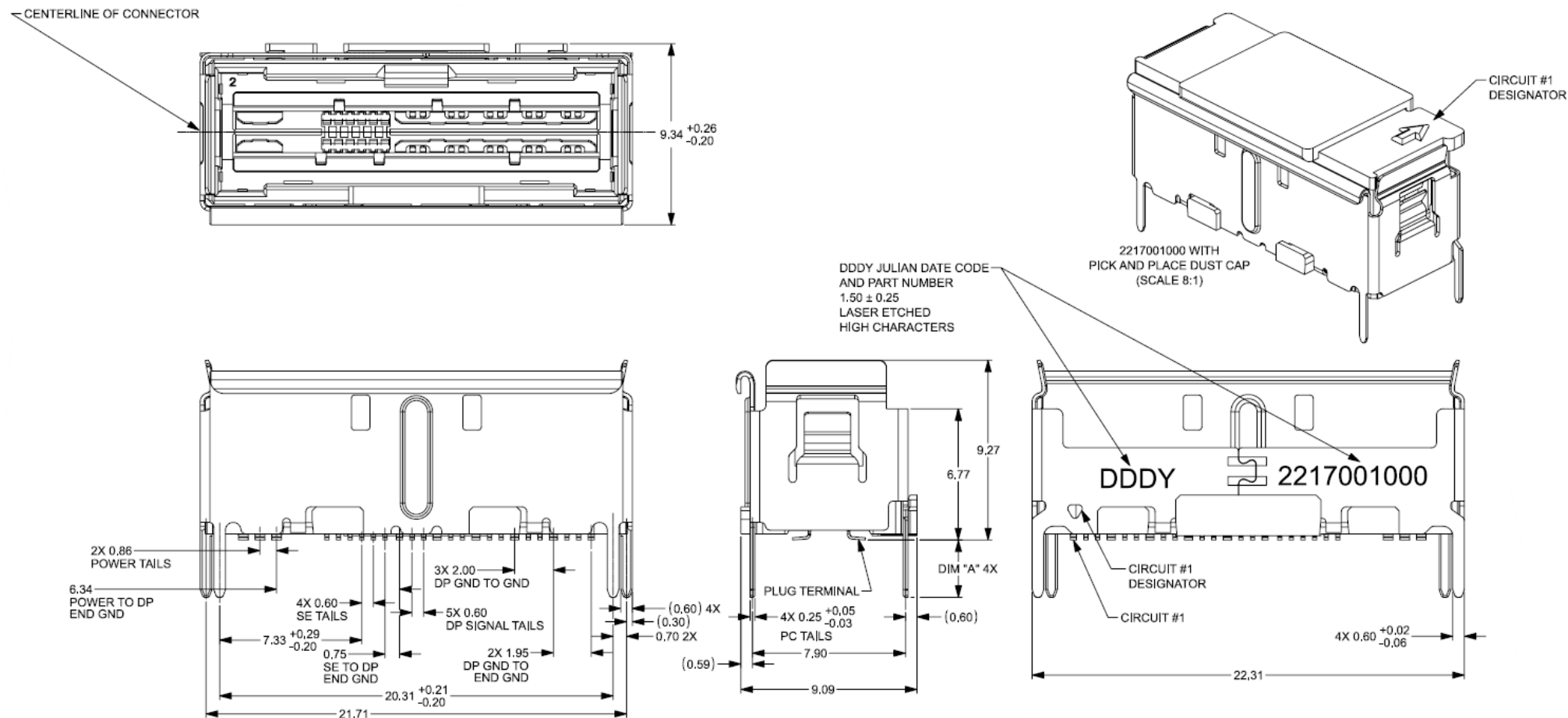
- To provide an internal cable and connector form factor to enable an optimized boot drive interconnect solution; support for high speed, low speed, and power transmission within the same cable and connector
- Features include direct-to-contact wire termination, optimized signal integrity and power delivery, maximized flexibility in system implementation
- Editor: Egidio Murisa [Molex]
- Supporters:
 1. Molex
 2. Intel
 3. Amphenol
 4. Dell

General Specification Scope:

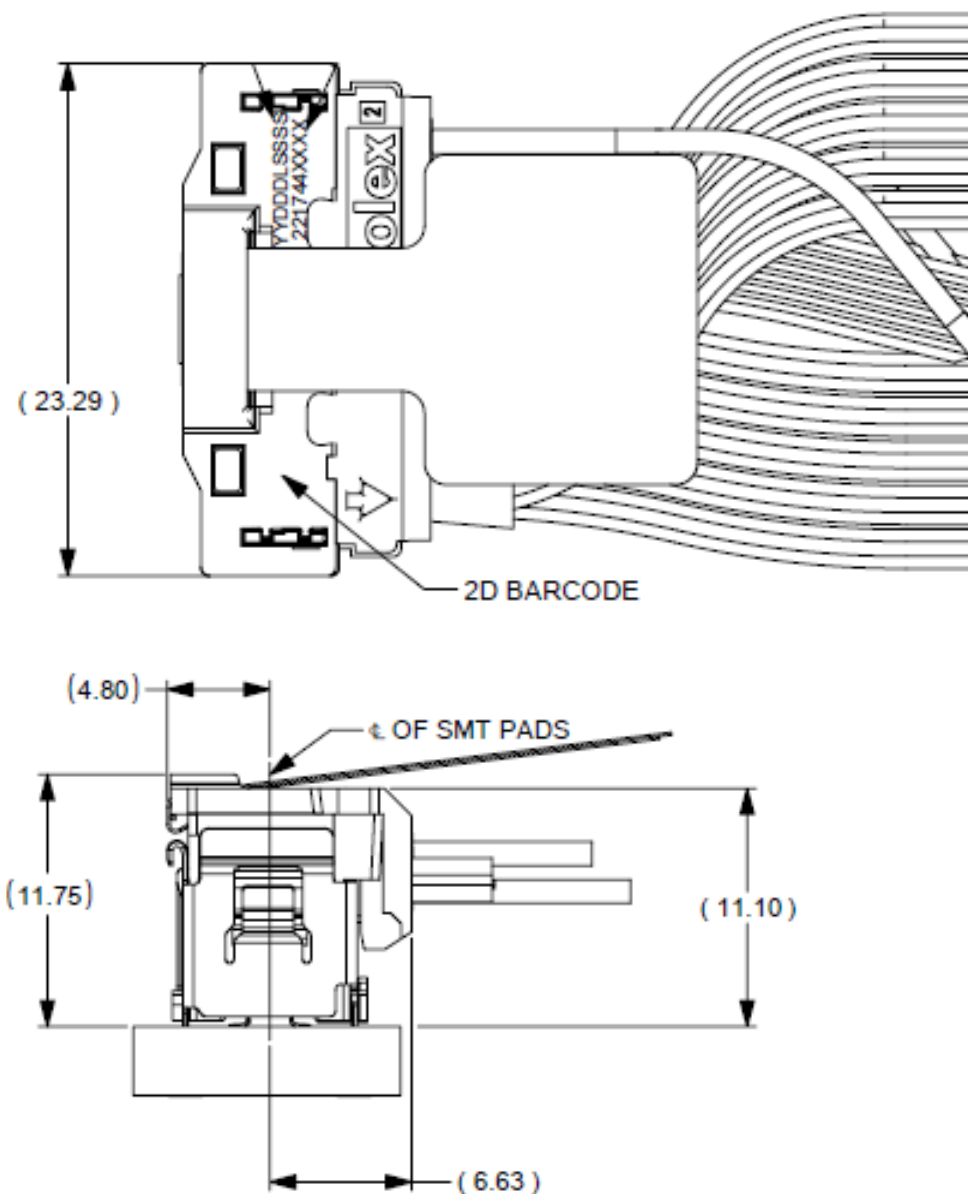
Form Factor:		
Pitch	0.6mm	
Receptacle Cable Type	Vertical Exit	Right Angle Exit
Board Connector (Plug) Type	Vertical (SMT & through Hole)	
Number of differential Pairs	10DP + 12SE + 4A PWR	
Cable AWG supported	30 AWG twinax (high speed); 34 AWG ribbon (low speed); 24 AWG power	
Electrical Specification:		
Impedance	85Ω	
Maximum Current Supported	4A per 3 power contact pins (1 power contact); 0.65A on 30 AWG wire; 0.30A on 34 AWG	



Board Connector (Plug):

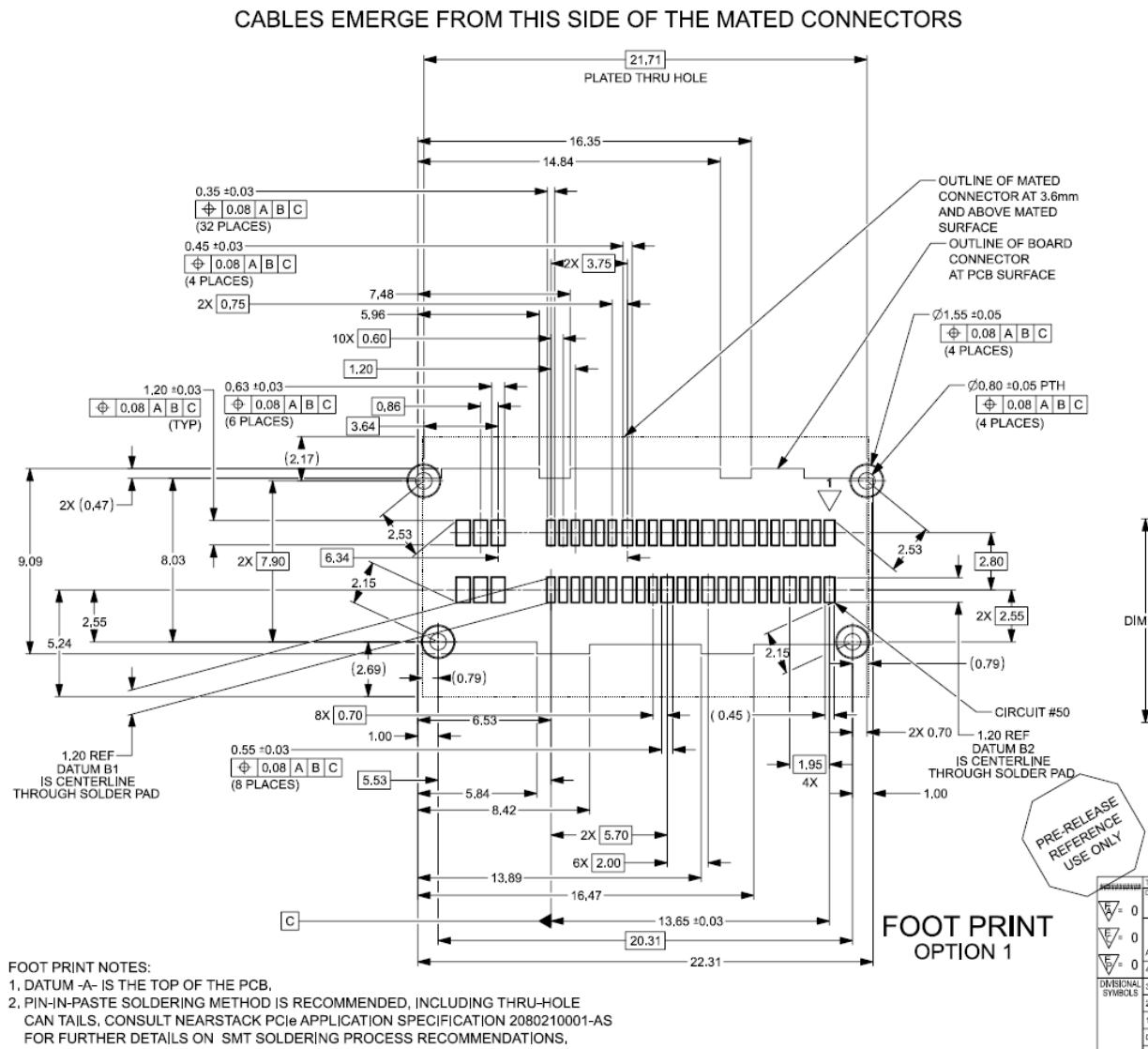


Mating Dimensions:



Board Connector (Plug) Footprint:

- Connector footprint will be included as a normative feature to ensure PCB cross-compatibility across any/ all suppliers



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- IP Declaration:

- NOTE: The document will undergo an IP disclosure period prior to publication per the SNIA SFF Process Guide