

USED ON

D

C

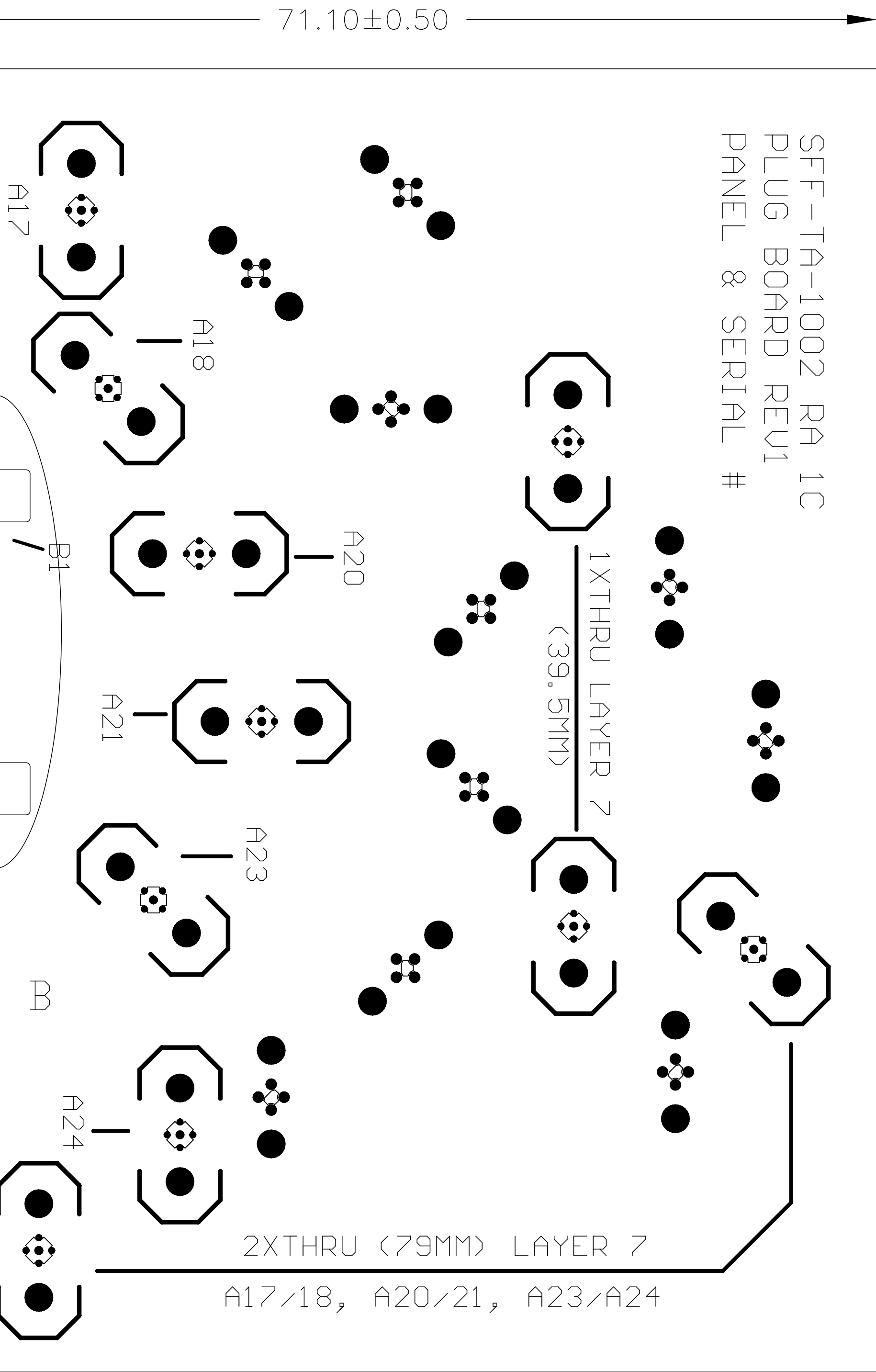
B

A

SFF-TA-1002 RA 1C
PLUG BOARD REV1
PANEL & SERIAL #

1XTHRU LAYER 2
(39.5MM)

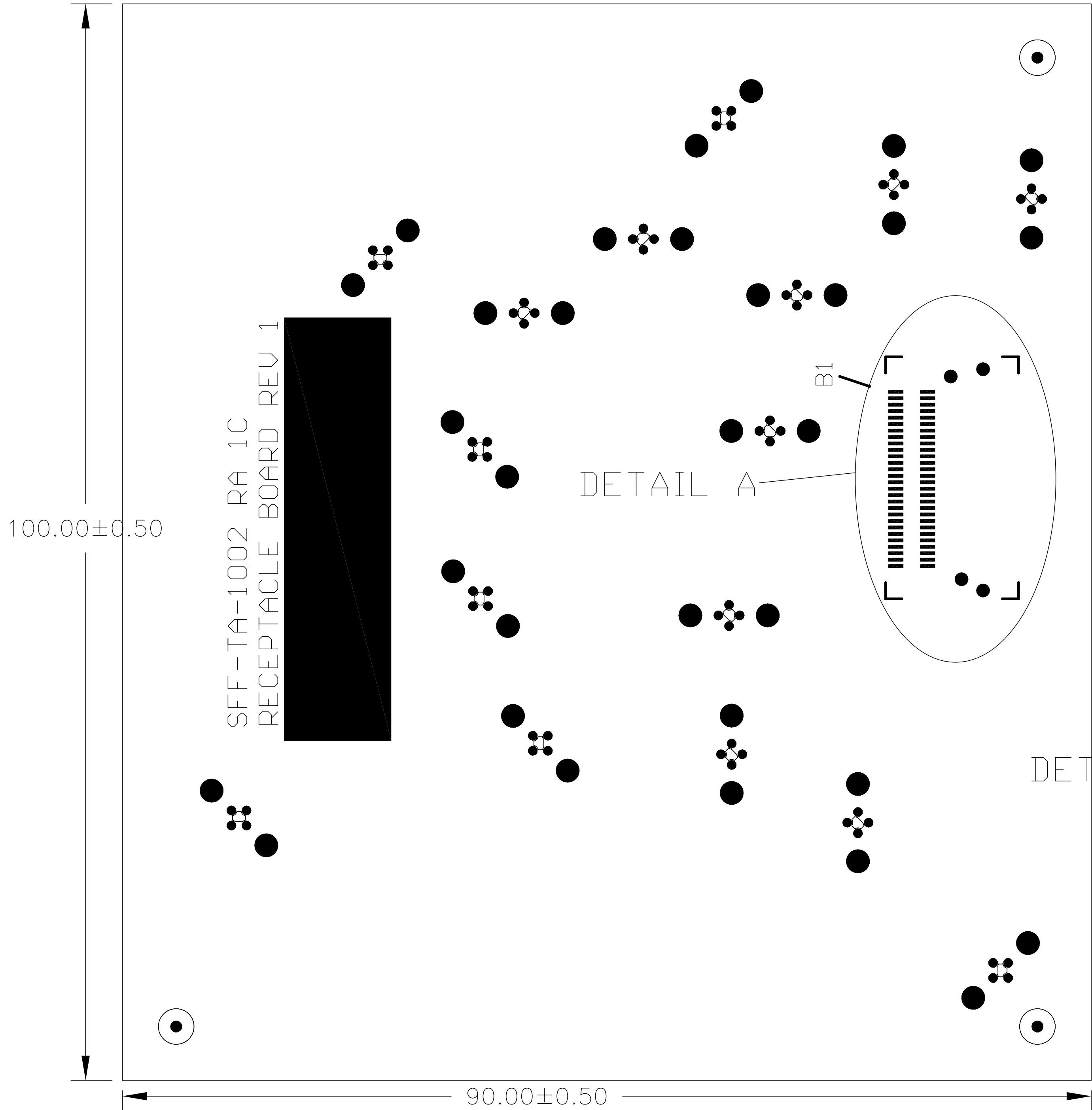
2XTHRU (79MM) LAYER 2
A17/18, A20/21, A23/A24



B1

DETAIL A

DETAIL B



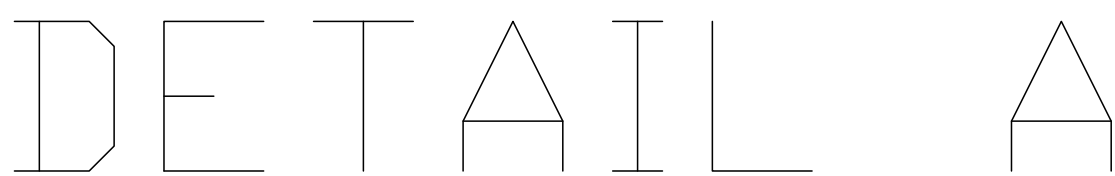
DWN	-	MATERIAL	-	HEAT TREAT	-
CHK	-				
APVD	-				
NAME	-	PRINTED CIRCUIT BOARD TEST FIXTURE SFF-TA-1002 RA 1C			
SCALE	2:1	SIZE	A1	DRAWING NO	SFF-TA-1002 RA 1C
SHEET	2	OF	4	REV	1

DIMENSIONS:	TOLERANCES UNLESS OTHERWISE SPECIFIED:
mm	0 PLG ±.05
	1 PLG ±.05
	2 PLG ±.05
	3 PLG ±.05
	4 PLG ±.05

ANGLES ±	-
SURFACE TEXTURE	√

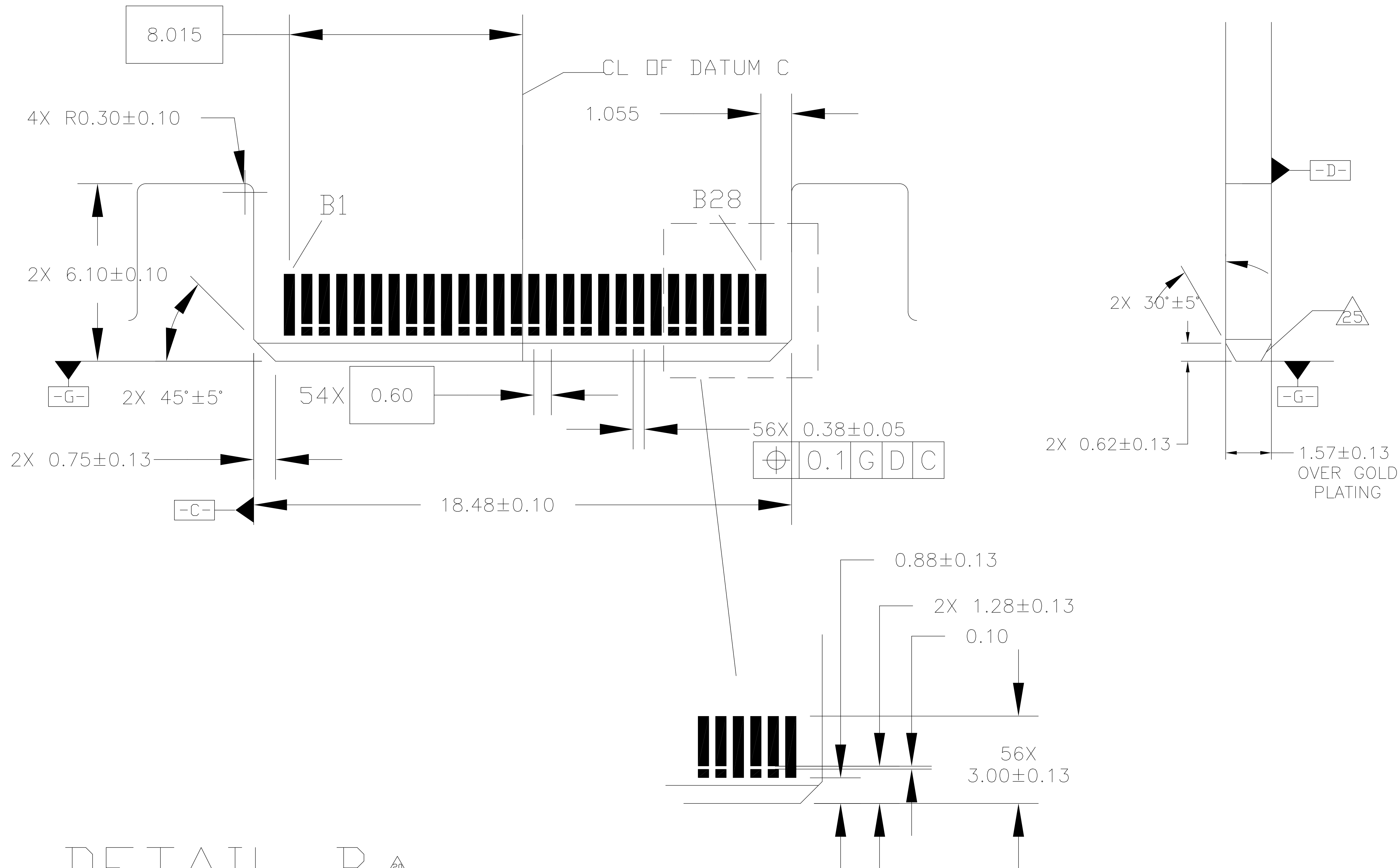
REVISION RECORD	DATE	DWN	APVD
SEE SHEET 1	-	-	-

OWG NO
SFF-TA-1002 RA 1C



DWN	—	MATERIAL	—	HEAT TREAT	—
CHK	—	—	—	—	—
APVD	—				
NAME					
PRINTED CIRCUIT BOARD, TEST FIXTURE					
SFF-1A-1002 RA 1C					
SCALE	SIZE	DRAWING NO	SHEET	OF	REV
2:1	A1	SFF-1A-1002 RA 1C	3	4	1

DETAIL B



DWN	---	MATERIAL	---	HEAT TREAT	---
CHK	---		---		---
APVD	---				
NAME	PRINTED CIRCUIT BOARD TEST FIXTURE SFF-TA-1002 RA 1C				
SCALE	SIZE	DRAWING NO	SHEET	OF	REV
2:1	A1	SFF-TA-1002 RA 1C	4	4	1