
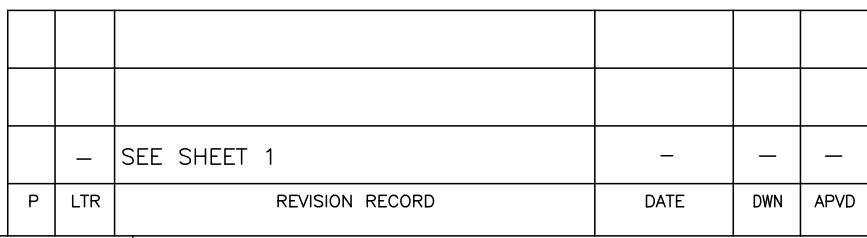
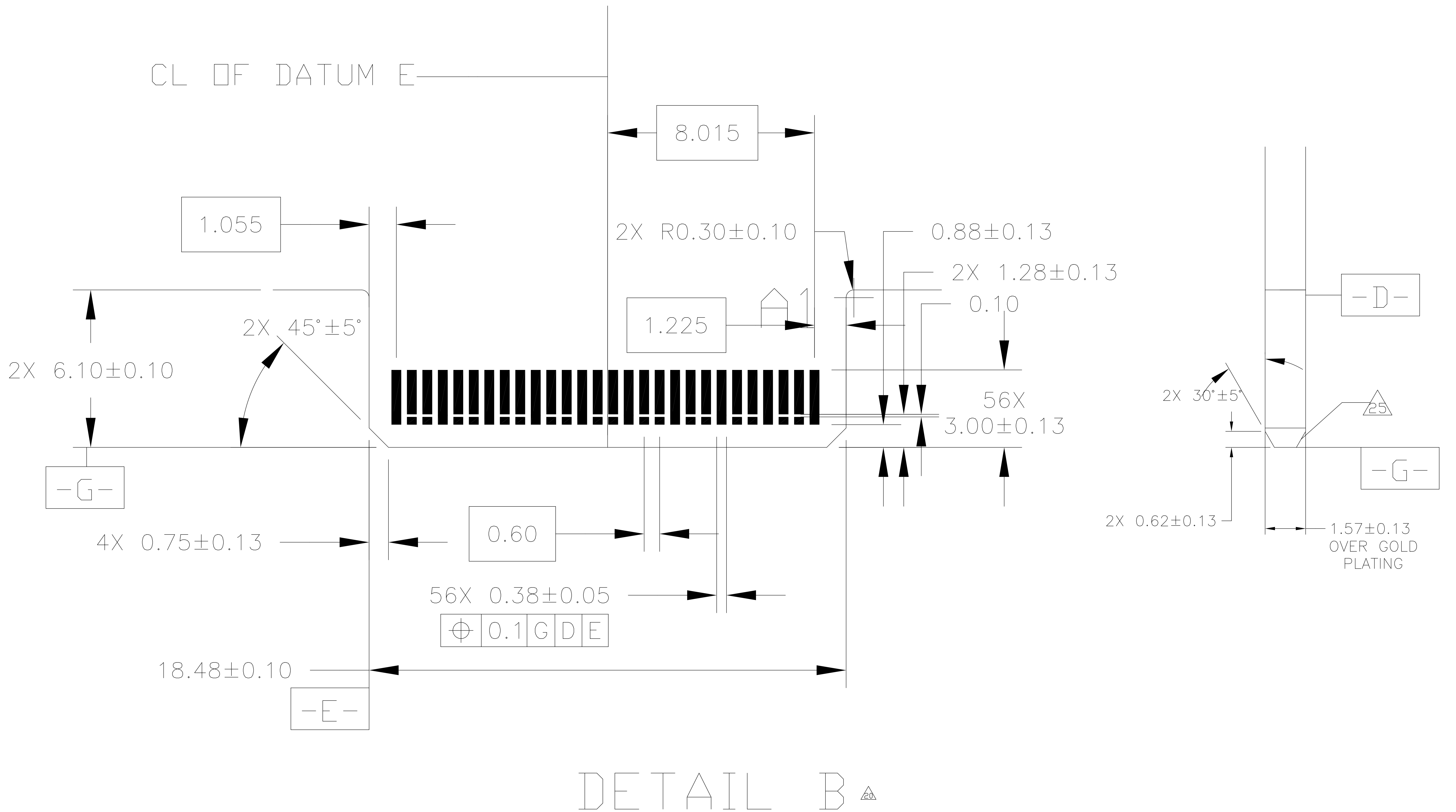




DWN S.PATEL	16MAY18	MATERIAL	—	HEAT TREAT	—
CHK —	—		—	—	—
APVD —	—	 TE Connectivity			
—	—				
NAME					
PRINTED CIRCUIT BOARD, TEST FIXTURE SFF-TA-1002_VERTICAL_1C					
SCALE 2:1	SIZE A1	DRAWING NO SFF-TA-1002_Vert_1C		SHEET 2	OF 4
				REV 1	



DWN	18MAY18	MATERIAL	HEAT TREAT
S.PATEL		-	-
CHK		-	-
APVD			
NAME	PRINTED CIRCUIT BOARD, TEST FIXTURE SFF-TA-1002 VERTICAL 1C		
SCALE	SIZE	DRAWING NO	SHEET
2:1	A1	SFF-TA-1002_Vert_1C	3 OF 4
			REV 1



	—	SEE SHEET 1	—	—	—
P	LTR	REVISION RECORD	DATE	DWN	APV

DIMENSIONS:		TOLERANCES UNLESS OTHERWISE SPECIFIED	
mm		0 PLC	± .05
		1 PLC	± .05
		2 PLC	± .05
		3 PLC	± .05
		4 PLC	± .05
ANGLES ±	-		
SURFACE TEXTURE			

DWN	18 MAY 18	MATERIAL	—	HEAT TREAT	—
S.PATEL			—		—
CHK	—				
—					
APVD	—				
—					
NAME					
PRINTED CIRCUIT BOARD, TEST FIXTURE					
SFF-TA-1002_VERTICAL					
SCALE	SIZE	DRAWING NO	SHEET	OF	REV
2:1	A1	SFF-TA-1002_Vert_1C	4	4	1