SFF-TA-1006

Specification for

Enterprise and Datacenter 1U Short Device Form Factor (E1.S)

Rev 1.5 August 6, 2021

SECRETARIAT: SFF TA TWG

This specification is made available for public review at https://www.snia.org/sff/specifications. Comments may be submitted at https://www.snia.org/feedback. Comments received will be considered for inclusion in future revisions of this specification.

The description of the form factor in this specification does not assure that the specific component is available from suppliers. If such a form factor is supplied, it should comply with this specification to achieve interoperability between suppliers.

ABSTRACT: This specification defines the mechanical attributes of a 1U short form factor for a device with multiple thicknesses that will fit in vertically in standard 1U rack mounted host systems.

This specification provides a common reference for host systems manufacturers, host system integrators, and device suppliers. This specification originates from Enterprise and Datacenter SSD Form Factor Working Group (EDSFF). Based on non-SSD devices also using EDSFF and agreement from the EDSFF Working Group, the SFF TA TWG agreed changing EDSFF to Enterprise and Datacenter Standard Form Factor.

The description of the device in this specification does not assure that the specific component is actually available from device suppliers. If such a device is supplied it shall comply with this specification to achieve interoperability between device suppliers.

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Foreword
The development work on this specification was done by the SNIA SFF TWG, an industry group. Since its formation as the SFF Committee in August 1990, the membership has included a mix of companies which are leaders across the industry.

For those who wish to participate in the activities of the SFF TWG, the signup for membership can be found at https://www.snia.org/sff/join.

Revision History
Rev 1.0   January 16, 2018:
- Initial release with editorial fixes to TOC and Fig 4-1 diagram

Rev 1.1   June 1, 2018:
- Updated IP section and foreword per current policy.
- Corrected ASME reference in 2.3 for consistency.
- Corrected dimension F1 in Table 4-1.
- Corrected Figure 4-2 to remove E dimension and changed the radius of the notches in Detail and B.
- Further updates to TOC

Rev 1.2   April 12, 2019
- Converted to the new SFF document template
- Added new name (E1.S).
- Clarified abstract.
- Section 3.3: Added definition for enclosure and modified definition of 1U.
- Section 5: Clarified power is a recommendation and added recommended power for optional heat spreader and optional enclosures.
- Section 5.1: Added clarification to bounding volume, surface dimensions, and rounding.
- Table 5-1: Modified Measurement C2 to align with SFF-TA-1002.
- Table 5-1: Modified Measurements D3 and D4.
- Table 5-1: Fixed the comment for Measurement F6.
- Section 5.4, 5.5: Added. This adds optional symmetric and asymmetric enclosure dimensions.
- Section 7: Added. This is an informative section on system thermal design guidelines.

Rev 1.3   July 17, 2019
- Section 5.1: Added statement to clarify that PCB dimensions are not required but highly recommended for enclosures specified in sections 5.4 and 5.5.
- Section 5.1 changed added wording for PCB in enclosures
- Figure 5.1: Added new dimension D5
- Figure 5.1: Added label for LED facing side
- Table 5.1: Clarified measurement for D1, D2 is to LED center position
- Table 5-1: Made E1-E7 BASIC to match the drawing
- Table 5.1: Added comment to drawing on mounting hole dependency to C2
- Section 5.4, 5.5, 6: Added x8 Card Edge along with Dimension B10 (Enclosure to x8 Datum F)
- Section 5.4, 5.5: Added Measurement C8 (Datum Y to Datum T), B11 (Datum W to LED center)
- Section 5.5: Added note to clarify Section 5.5 dimensions are equivalent to section 5.4
- Figure 5-3, 5-4: Add label to primary and secondary side
- Figure 5-3, 5-4: Change drawing ordering
- Figure 5-3, 5-4, Table 5-3: Added dimensions D6, D7, D8, E16
- Table 5-3: Fixed an error in value of B8.
- Table 5-4: Deleted dimensions B8 and B9. Redundant

Rev 1.4   March 27, 2020
- Added 15mm asymmetric thickness to section 5.5, corresponding thermal entry in Table 7.1 and descriptions in sections 5.1 and 7.1.
**Rev 1.5**

August 6, 2021

- Changed SSD to device and abstract edit to reflect EDSFF name change.
- Section 3.1: Change to definition of Restricted
- Section 5: Clarification on power and only PCB card edge is exposed outside the enclosure area.
- Section 5: Removal of default tolerance and added tolerances to Table 5-2.
- Section 5: Datum name change from “Y” to “G” to align with SFF-TA-1002 Datum.
- Section 5: Removal of Power references apart from section 5.1 recommendations.
- Section 5: Moved the power references in section 5.1 to an informative table.
- Section 5.1: Moved statements on mounting holes, defined hatch, and labels to Section 5.2
- Section 5.1: Added statement allowing security labels to be placed anywhere on the enclosure.
- Table 5-1: Changed C2 note to cover 7.5 mm instead of 7 mm.
- Table 5-1: Removed x, y references to mounting hole measurements.
- Table 5-1: Removed dimensions in comments for the cutouts.
- Table 5-2: Removed heat spreader option and x, y references in the comments.
- Figure 5-4: Added Phi to E14
- Section 5.4, 5.5: Changed note 1 wording.
- Section 5.4, 5.5: Added note on security label being allowed in label keep out region.
- Section 5.4, 5.5: Changed PCB expose from shall to should. Intent to make shall in future revision.
- Section 5.4, 5.5: Clarification that both 1C and 2C card edges are allowed.
- Section 5.4, 5.5: Removed measurement E16 (REF dimension) and moved C8 to a different view.
- Section 5.4, 5.5: Note added for recommended ground contact.
- Section 7: Deleted informative thermal guidance. Replaced with power and thermal requirements.
- Table 7-1: Added an entry for bare PCB vs. enclosure-based device.
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1. Scope
This specification defines the mechanical attributes of a new form factor for a device that will fit in 1U rack mounted host systems designed to support this form factor.

1.1 Application Specific Criteria
This 1U short form factor provides external dimensions, card edge placement, grounded mechanical mounting hole locations and LED placement to assist host system manufacturers in integration of this form factor.

The environment for the 1U short form factor is an enclosure connecting one or more devices in a dedicated packaging environment.

2. References and Conventions

2.1 Industry Documents
The following documents are relevant to this specification:
- SFF-TA-1002 Protocol Agnostic Multi-Lane High Speed Connector specification
- SFF-TA-1009 Enterprise and Datacenter Standard Form Factor Pin and Signal Specification
- SFF-TA-1023 Thermal Characterization Specification for EDSFF Devices

2.2 Sources
The complete list of SFF documents which have been published, are currently being worked on, or that have been expired by the SFF Committee can be found at https://www.snia.org/sff/specifications. Suggestions for improvement of this specification will be welcome, they should be submitted to https://www.snia.org/feedback.

Other standards may be obtained from the organizations listed below:

<table>
<thead>
<tr>
<th>Standard</th>
<th>Organization</th>
<th>Website</th>
</tr>
</thead>
<tbody>
<tr>
<td>ASME</td>
<td>American Society of Mechanical Engineers (ASME)</td>
<td><a href="https://www.asme.org">https://www.asme.org</a></td>
</tr>
</tbody>
</table>
2.3 Conventions
The following conventions are used throughout this document:

DEFINITIONS
Certain words and terms used in this standard have a specific meaning beyond the normal English meaning. These words and terms are defined either in the definitions or in the text where they first appear.

ORDER OF PRECEDENCE
If a conflict arises between text, tables, or figures, the order of precedence to resolve the conflicts is text; then tables; and finally figures. Not all tables or figures are fully described in the text. Tables show data format and values.

DIMENSIONING CONVENTIONS
The dimensioning conventions are described in ASME-Y14.5, Geometric Dimensioning and Tolerancing. All dimensions are in millimeters, which are the controlling dimensional units (if inches are supplied, they are for guidance only).

NUMBERING CONVENTIONS
The ISO convention of numbering is used (i.e., the thousands and higher multiples are separated by a space and a period is used as the decimal point). This is equivalent to the English/American convention of a comma and a period.

<table>
<thead>
<tr>
<th>American</th>
<th>French</th>
<th>ISO</th>
</tr>
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</tr>
<tr>
<td>1,000</td>
<td>1 000</td>
<td>1 000</td>
</tr>
<tr>
<td>1,323,462.9</td>
<td>1 323 462,9</td>
<td>1 323 462.9</td>
</tr>
</tbody>
</table>
3. Keywords, Acronyms, and Definitions

For the purposes of this document, the following keywords, acronyms, and definitions apply.

3.1 Keywords

**May:** Indicates flexibility of choice with no implied preference.

**May or may not:** Indicates flexibility of choice with no implied preference.

**Obsolete:** Indicates that an item was defined in prior specifications but has been removed from this specification.

**Optional:** Describes features which are not required by the SFF specification. However, if any feature defined by the SFF specification is implemented, it shall be done in the same way as defined by the specification. Describing a feature as optional in the text is done to assist the reader.

**Prohibited:** Describes a feature, function, or coded value that is defined in a referenced specification to which this SFF specification makes a reference, where the use of said feature, function, or coded value is not allowed for implementations of this specification.

**Reserved:** Defines the signal on a connector contact [when] its actual function is set aside for future standardization. It is not available for vendor specific use. Where this term is used for bits, bytes, fields, and code values; the bits, bytes, fields, and code values are set aside for future standardization. The default value shall be zero. The originator is required to define a Reserved field or bit as zero, but the receiver should not check Reserved fields or bits for zero.

**Restricted:** Refers to features, bits, bytes, words, and fields that are set aside for other standardization purposes. If the context of the specification applies the restricted designation, then the restricted bit, byte, word, or field shall be treated as a value whose definition is not in scope of this document, and is not interpreted by this specification.

**Shall:** Indicates a mandatory requirement. Designers are required to implement all such mandatory requirements to ensure interoperability with other products that conform to this specification.

**Should:** Indicates flexibility of choice with a strongly preferred alternative.

**Vendor specific:** Indicates something (e.g., a bit, field, code value) that is not defined by this specification. Specification of the referenced item is determined by the manufacturer and may be used differently in various implementations.

3.2 Acronyms and Abbreviations

**EDSFF:** Enterprise and Datacenter Standard Form Factor

**NVM:** Non-Volatile Memory

**SSD:** Solid State Drive
3.3 Definitions

1U: 1 Standard Unit or Rack Unit 44.45 mm (1.75 inches).

Card: Refers to the device plugged into a connector

Device: Refers to the interface target

Enclosure: The housing that protects the internal components and acts as a heat sink.

Host: Refers to the interface source or initiator

Thickness: Form factor dimension including PCB thickness, z-height of all components plus mechanicals.
4. General Description

4.1 Configuration Overview/Descriptions

The application environment for the 1U short form factor is a cabinet or enclosure connecting to one or more add-in cards. 1U refers to 1 standard unit of an IT equipment rack and the IT enclosures that fit in this space. The device form factor is intended for use in enclosures that fit within that given space. The primary usage is for datacenter server and storage systems that require high capacity and performance highly scalable in 1U. The device connects electrically to the system through a card edge connector as defined in SFF-TA-1002. There are multiple thicknesses of the 1U short form factor depending on the max power rating. The definition of mounting holes and component placement area allows for attachment of mechanicals to adapt among different enclosure chassis, such as rails and latching. The form factor is designed not to require a fully enclosed case, but outer dimensions of a case version are included for compatibility if one is desired. Figure 4-1 represents an example of a system implementation using the 1U short form factor.

Figure 4-1. Example systems showing implementations of 1U short form factor.
5. Mechanical Specification

5.1 Overview

This section specifies the dimensions for the 1U short form factor. There are multiple thicknesses specified:

- A 5.9 mm thick form factor
- An 8.01 mm thick form factor with an optional heat spreader
- A 9.5 mm thick form factor with an optional symmetrical enclosure
- A 15 mm thick form factor with an optional asymmetric enclosure
- A 25 mm thick form factor with an optional asymmetric enclosure

No part of the host chassis/guide rails of a host enclosure or parts connected to the mounting holes (e.g., a latch) should encroach into any part of the bounding volume of the device form factor dimensions and tolerances as specified in this standard when the device is inserted into the host enclosure.

Unless specified, the default tolerance is +/- 0.15 mm. All dimensions provided in mm.

For the label placement and fin area, dimensions for a surface apply to a single point minimum. If a surface is not flat, the dimension applies to the highest raised location on that surface. Except for the card edge connector, each defined edge may have rounding.

The form factors specified in Section 5.2 and Section 5.3 may be used within the enclosures specified in Section 5.4 and 5.5, but is not required. The PCB with 2C (x8) card edge is not required to meet Section 5.2.
5.2 Physical Definition: 1U Short Form Factor

All specified mounting holes shall be grounded and mechanical attachment should not exceed radius of defined copper pads. The defined hatched area is component placement area. Unless specified, any labels must be in component placement or optional heat spreader area.

Figure 5-1. 5.9 mm Thick 1U Short Form Factor drawing
### 5.2.1 1U Short Form Factor

#### Table 5-1. 1U Short Form Factor Dimensions

<table>
<thead>
<tr>
<th>Dimensions</th>
<th>Millimeters</th>
<th>Tolerance</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>A1</td>
<td>5.9</td>
<td>MAX</td>
<td>Maximum device thickness (reference)</td>
</tr>
<tr>
<td>A2</td>
<td>2.10</td>
<td>MAX</td>
<td>Maximum component height</td>
</tr>
<tr>
<td>A3</td>
<td>1.57</td>
<td>0.13</td>
<td>PCB Card Edge thickness (ref: see SFF-TA-1002)</td>
</tr>
<tr>
<td>B1</td>
<td>31.5</td>
<td>0.2</td>
<td>Device height with defined cutouts</td>
</tr>
<tr>
<td>B2</td>
<td>11.23</td>
<td>0.15</td>
<td>Card Bottom Edge to centerline of Datum E</td>
</tr>
<tr>
<td>B3</td>
<td>3.21</td>
<td>0.15</td>
<td>Center of Connector Pin A1 location from PCB (reference)</td>
</tr>
<tr>
<td>C1</td>
<td>111.49</td>
<td>0.15</td>
<td>Add in card Length</td>
</tr>
<tr>
<td>C2</td>
<td>6</td>
<td>Min</td>
<td>Card edge length. Note if dimension is greater than 7.5 mm, mounting hole 3 is allowed to be a half-moon</td>
</tr>
<tr>
<td>D1</td>
<td>10.6</td>
<td>0.15</td>
<td>Power and activity (Green) LED lens mechanical center position from Datum X</td>
</tr>
<tr>
<td>D2</td>
<td>10.6</td>
<td>0.15</td>
<td>Attention or error (Amber) LED lens mechanical center position from Datum X</td>
</tr>
<tr>
<td>D3</td>
<td>0.5</td>
<td>0.2</td>
<td>Power and activity (Green) LED lens mechanical center position from PCB.</td>
</tr>
<tr>
<td>D4</td>
<td>0.5</td>
<td>0.2</td>
<td>Attention or error (Amber) LED lens mechanical center position from PCB.</td>
</tr>
<tr>
<td>D5</td>
<td>110.49</td>
<td>0.45</td>
<td>LED edge closest to latch area</td>
</tr>
<tr>
<td>E1</td>
<td>12.95</td>
<td>BASIC</td>
<td>Mounting hole 1</td>
</tr>
<tr>
<td>E2</td>
<td>2.5</td>
<td>BASIC</td>
<td>Mounting hole 1 and 2</td>
</tr>
<tr>
<td>E3</td>
<td>107.19</td>
<td>BASIC</td>
<td>Mounting hole 2</td>
</tr>
<tr>
<td>E5</td>
<td>8.95</td>
<td>BASIC</td>
<td>Mounting hole 3</td>
</tr>
<tr>
<td>E6</td>
<td>29</td>
<td>BASIC</td>
<td>Mounting hole 3 and 4</td>
</tr>
<tr>
<td>E7</td>
<td>106.19</td>
<td>BASIC</td>
<td>Mounting hole 4</td>
</tr>
<tr>
<td>F1</td>
<td>18.95</td>
<td>0.15</td>
<td>Cutout 1</td>
</tr>
<tr>
<td>F2</td>
<td>47.15</td>
<td>0.15</td>
<td>Cutout 2</td>
</tr>
<tr>
<td>F3</td>
<td>100.69</td>
<td>0.15</td>
<td>Cutout 3</td>
</tr>
<tr>
<td>F4</td>
<td>22.95</td>
<td>0.15</td>
<td>Cutout 4</td>
</tr>
<tr>
<td>F5</td>
<td>48.15</td>
<td>0.15</td>
<td>Cutout 5</td>
</tr>
<tr>
<td>F6</td>
<td>100.69</td>
<td>0.15</td>
<td>Cutout 6</td>
</tr>
<tr>
<td>F7</td>
<td>8</td>
<td>0.15</td>
<td>Width of cutout 2</td>
</tr>
<tr>
<td>F8</td>
<td>6</td>
<td>0.15</td>
<td>Width of cutout 5</td>
</tr>
</tbody>
</table>
5.3 Physical Definition: 1U Short Form Factor with Optional Heat Spreader

![Diagram of 1U Short Form Factor with Optional Heat Spreader]

Figure 5-2. 1U Short Form Factor drawing with heat spreader option

5.3.1 1U Short Form Factor with Optional Heat Spreader

<table>
<thead>
<tr>
<th>Dimensions</th>
<th>Millimeters</th>
<th>Tolerance</th>
<th>Comment</th>
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</thead>
<tbody>
<tr>
<td>A4</td>
<td>8.01</td>
<td>0.15</td>
<td>Device thickness including heat spreader (reference)</td>
</tr>
<tr>
<td>A5</td>
<td>3.22</td>
<td>0.25</td>
<td>PCB to outer thickness for heat spreader</td>
</tr>
<tr>
<td>J1</td>
<td>16.25</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>J2</td>
<td>103.89</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>J3</td>
<td>12.25</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>J4</td>
<td>102.89</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>J5</td>
<td>110.49</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>K1</td>
<td>3.7</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>K2</td>
<td>5.8</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>K3</td>
<td>5.8</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>K4</td>
<td>25.7</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>K5</td>
<td>25.7</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
<tr>
<td>K6</td>
<td>27.8</td>
<td>0.15</td>
<td>Heat spreader dimension</td>
</tr>
</tbody>
</table>
5.4 Physical Definition: 1U Short Form Factor with Optional Symmetric Enclosure

The PCB excluding the card edge should not extend past the defined enclosure area. The device defined in the following section supports either the 1C or 2C card edge as defined in SFF-TA-1002. The area conductive to ground may be larger than what is documented. Labels shall be placed in the label placement area. Security labels are permitted on any surface of the enclosure.

Notes:
1. Host should make grounding contact to at least 1 of these surfaces on the LED facing side.

Figure 5-3. 1U Short Form Factor drawing with Optional Symmetric Enclosure
### 5.4.1 1U Short Form Factor with Optional Symmetric Enclosure

Table 5-3. 1U Short Form Factor – Optional Symmetric Enclosure Dimensions

<table>
<thead>
<tr>
<th>Dimensions</th>
<th>Millimeters</th>
<th>Tolerance</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>A6</td>
<td>9.5</td>
<td>0.35</td>
<td>Device Thickness including enclosure</td>
</tr>
<tr>
<td>A7</td>
<td>3.96</td>
<td>0.15</td>
<td>PCB at LED and card edge connector to outer thickness</td>
</tr>
<tr>
<td>A8</td>
<td>0.4</td>
<td>MAX</td>
<td>Straightness</td>
</tr>
<tr>
<td>B4</td>
<td>33.75</td>
<td>0.25</td>
<td>Device width</td>
</tr>
<tr>
<td>B5</td>
<td>4.4</td>
<td>REF</td>
<td>Center - Connector Pin A1 location from Datum X</td>
</tr>
<tr>
<td>B6</td>
<td>12.415</td>
<td>0.35</td>
<td>Control dimension for x4 card edge; TA-1002 DATUM “E”</td>
</tr>
<tr>
<td>B7</td>
<td>0.4</td>
<td>MAX</td>
<td>Straightness</td>
</tr>
<tr>
<td>B8</td>
<td>23.75</td>
<td>0.25</td>
<td>Label/Fin placement region</td>
</tr>
<tr>
<td>B9</td>
<td>5</td>
<td>REF</td>
<td>Host alignment structure region (reference)</td>
</tr>
<tr>
<td>B10</td>
<td>22.605</td>
<td>0.35</td>
<td>Control dimension for x8 card edge; TA-1002 DATUM “F”</td>
</tr>
<tr>
<td>B11</td>
<td>11.79</td>
<td>0.35</td>
<td>Datum “W” to LED center position</td>
</tr>
<tr>
<td>C3</td>
<td>118.75</td>
<td>0.55</td>
<td>Device length</td>
</tr>
<tr>
<td>C4</td>
<td>112.5</td>
<td>+0.15/-0.95</td>
<td>Datum “G” to latch area keep out zone</td>
</tr>
<tr>
<td>C5</td>
<td>12</td>
<td>MIN</td>
<td>Minimum Conductive area length</td>
</tr>
<tr>
<td>C6</td>
<td>35</td>
<td>0.15</td>
<td>Bottom conductive area 1 x position</td>
</tr>
<tr>
<td>C7</td>
<td>3.2</td>
<td>MIN</td>
<td>Bottom conductive area length</td>
</tr>
<tr>
<td>C8</td>
<td>7.5</td>
<td>0.25</td>
<td>Datum “G” to Datum “T” (edge of enclosure)</td>
</tr>
<tr>
<td>D6</td>
<td>110.49</td>
<td>0.45</td>
<td>LED edge closest to latch area</td>
</tr>
<tr>
<td>D7</td>
<td>2.07</td>
<td>0.48</td>
<td>Green LED center position</td>
</tr>
<tr>
<td>D8</td>
<td>0.5</td>
<td>0.35</td>
<td>Amber LED center position</td>
</tr>
<tr>
<td>E8</td>
<td>3.55</td>
<td>BASIC</td>
<td>Mounting Hole 1 g position</td>
</tr>
<tr>
<td>E9</td>
<td>30.05</td>
<td>BASIC</td>
<td>Mounting Hole 2 g position</td>
</tr>
<tr>
<td>E10</td>
<td>108</td>
<td>BASIC</td>
<td>Mounting Hole 1 x and 2 x position</td>
</tr>
<tr>
<td>E11</td>
<td>2.7</td>
<td>0.15</td>
<td>Mounting Thru Hole Diameter</td>
</tr>
<tr>
<td>E12</td>
<td>4.7</td>
<td>0.15</td>
<td>Mounting Counterbore Diameter</td>
</tr>
<tr>
<td>E13</td>
<td>1.2</td>
<td>0.1</td>
<td>Mounting Counterbore Depth</td>
</tr>
<tr>
<td>E14</td>
<td>0.25</td>
<td>MAX</td>
<td>Position Tolerance</td>
</tr>
<tr>
<td>E15</td>
<td>2.2</td>
<td>0.15</td>
<td>Latch mounting area thickness</td>
</tr>
</tbody>
</table>
5.5 Physical Definition: 1U Short Form Factor with Optional Asymmetric Enclosure

Unless specified in Table 5-4, dimensions are the same as in Table 5-3. There are two thicknesses denoted in Table 5-4. The PCB excluding the card edge should not extend past the defined enclosure area. The device defined in the following section supports either the 1C or 2C card edge as defined in SFF-TA-1002. The area conductive to ground may be larger than what is documented. Labels shall be placed in the label placement area. Security labels are permitted on any surface of the enclosure.

Notes:
1. Host should make grounding contact to at least 1 of these surfaces on the LED facing side.

Figure 5-4. 1U Short Form Factor drawing with Optional Asymmetric Enclosure
### 5.5.1 1U Short Form Factor with Optional Asymmetric Enclosure

Table 5-4. 1U Short Form Factor – Optional Asymmetric Enclosure Dimensions

<table>
<thead>
<tr>
<th>Dimensions</th>
<th>Millimeters</th>
<th>Tolerance</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>A9a</td>
<td>25</td>
<td>+0.35/-0.60</td>
<td>Device Thickness including thick enclosure (25 mm)</td>
</tr>
<tr>
<td>A10a</td>
<td>19.46</td>
<td>REF</td>
<td>PCB to outer thickness with Heatsink (25 mm)</td>
</tr>
<tr>
<td>A9b</td>
<td>15</td>
<td>+0.35/-0.60</td>
<td>Device Thickness including thick enclosure (15 mm)</td>
</tr>
<tr>
<td>A10b</td>
<td>9.46</td>
<td>REF</td>
<td>PCB to outer thickness with Heatsink (15 mm)</td>
</tr>
</tbody>
</table>
6. Informative: SFF-TA-1002 edge (plug) Mechanical drawing

6.1 Overview
This section shows the card edge mechanical drawing for convenience only. See SFF-TA-1002 for normative and performance requirements.

![Figure 6-1. 1C (x4) Mating Card Dimensions](image1)

Note: Position A1 on opposite side of card of B1

![Figure 6-2. 2C (x8) Mating Card Dimensions](image2)

Note: Position A1 on opposite side of card of B1
The following section covers the power and thermal requirements of the device.

7.1 Power
Table 7-1 defines the initial slot power limit for the device. For more details about this and other power requirements, refer to SFF-TA-1009 Enterprise and Datacenter Standard Form Factor Pin and Signal Specification.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>E1.S (5.9/8.01 mm Thickness)</th>
<th>E1.S (9.5/15/25 mm Thickness)</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>Initial Slot Power Limit (12Vpinit)</td>
<td>12 W</td>
<td>25 W</td>
<td>Refer to SFF-TA-1009 for definition</td>
</tr>
</tbody>
</table>

7.2 Thermals
For detailed device thermal requirements, refer to SFF-TA-1023 Thermal Specification for EDSFF Devices.

7.3 Informative: Recommended Max Power
Table 7-2 defines the recommended maximum sustained power allowed by each device variation.

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>12Vpsus</td>
<td>12 W</td>
<td>16 W</td>
<td>20 W</td>
<td>25 W</td>
<td>Refer to SFF-TA-1009 for definition</td>
</tr>
</tbody>
</table>