SFF specifications are available at http://www.snia.org/sff/specifications
or ftp://ftp.seagate.com/sff

This specification was developed by the SFF Committee prior to it becoming the SFF TA (Technology Affiliate) TWG (Technical Working Group) of SNIA (Storage Networking Industry Association).

The information below should be used instead of the equivalent herein.

POINTS OF CONTACT:

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Email: SFF-Chair@snia.org

If you are interested in participating in the activities of the SFF TWG, the membership application can be found at:
http://www.snia.org/sff/join

The complete list of SFF Specifications which have been completed or are currently being worked on can be found at:
http://www.snia.org/sff/specifications/SFF-8000.TXT

The operations which complement the SNIA's TWG Policies & Procedures to guide the SFF TWG can be found at:
http://www.snia.org/sff/specifications/SFF-8032.PDF

Suggestions for improvement of this specification will be welcome, they should be submitted to:
http://www.snia.org/feedback
SFF Committee documentation may be purchased in hard copy or electronic form.
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SFF Committee

SFF-8075 Specification for

PCI Card Version of SFP Cage

Rev 1.0    July 3, 2001

Secretariat:  SFF Committee

Abstract:  This specification defines the unique mechanical parameters of the PCI Card version of the SFP (Small Form-factor Pluggable) Cage.

The original cage design defined by the MSA (Multi Source Agreement) is specified by INF-80741. This derivative enables implementation of the SFP modules in PCI Card based systems.

This document provides a common specification for systems manufacturers, system integrators, and suppliers. This is an internal working document of the SFF Committee, an industry ad hoc group.

This document is made available for public review, and written comments are solicited from readers. Comments received by the members will be considered for inclusion in future revisions of this document.

The description of the cage in this document does not assure that the specific component is actually available from suppliers. If such a cage is supplied it must comply with this specification to achieve interoperability between suppliers.

Support:  This document is supported by the identified member companies of the SFF Committee.

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EXPRESSION OF SUPPORT BY MANUFACTURERS

The following member companies of the SFF Committee voted in favor of this industry specification.

Adaptec
Compaq
DDK Fujikura
ENDL
FCI/Berg
Fujitsu CPA
Hewlett Packard
Hitachi GST
IBM
Molex
Seagate
Sun Microsystems
Toshiba America
Tyco AMP
Unisys

The following member companies of the SFF Committee voted to abstain on this industry specification.

Amphenol
Finisar
Intel

The user's attention is called to the possibility that implementation to this Specification may require use of an invention covered by patent rights. By distribution of this Specification, no position is taken with respect to the validity of this claim or of any patent rights in connection therewith. The patent holder has filed a statement of willingness to grant a license under these rights on reasonable and non-discriminatory terms and conditions to applicants desiring to obtain such a license.
If you are not a member of the SFF Committee, but you are interested in participating, the following principles have been reprinted here for your information.

**PRINCIPLES OF THE SFF COMMITTEE**

The SFF Committee is an ad hoc group formed to address storage industry needs in a prompt manner. When formed in 1990, the original goals were limited to defining de facto mechanical envelopes within which disk drives can be developed to fit compact computer and other small products.

Adopting a common industry size simplifies the integration of small drives (2 1/2" or less) into such systems. Board-board connectors carrying power and signals, and their position relative to the envelope are critical parameters in a product that has no cables to provide packaging leeway for the integrator.

In November 1992, the SFF Committee objectives were broadened to encompass other areas which needed similar attention, such as pinouts for interface applications, and form factor issues on larger disk drives. SFF is a forum for resolving industry issues that are either not addressed by the standards process or need an immediate solution.

Documents created by the SFF Committee are expected to be submitted to bodies such as EIA (Electronic Industries Association) or an ASC (Accredited Standards Committee). They may be accepted for separate standards, or incorporated into other standards activities.

The principles of operation for the SFF Committee are not unlike those of an accredited standards committee. There are 3 levels of participation:

- Attending the meetings is open to all, but taking part in discussions is limited to member companies, or those invited by member companies
- The minutes and copies of material which are discussed during meetings are distributed only to those who sign up to receive documentation.
- The individuals who represent member companies of the SFF Committee receive documentation and vote on issues that arise. Votes are not taken during meetings, only guidance on directions. All voting is by letter ballot, which ensures all members an equal opportunity to be heard.

Material presented at SFF Committee meetings becomes public domain. There are no restrictions on the open mailing of material presented at committee meetings. In order to reduce disagreements and misunderstandings, copies must be provided for all agenda items that are discussed. Copies of the material presented, or revisions if completed in time, are included in the documentation mailings.

The sites for SFF Committee meetings rotate based on which member companies volunteer to host the meetings. Meetings have typically been held during the ASC T10 weeks.

The funds received from the annual membership fees are placed in escrow, and are used to reimburse ENDL for the services to manage the SFF Committee.
If you are not receiving the documentation of SFF Committee activities or are interested in becoming a member, the following signup information is reprinted here for your information.

<table>
<thead>
<tr>
<th>Service</th>
<th>Fee</th>
</tr>
</thead>
<tbody>
<tr>
<td>Annual SFF Committee Membership Fee</td>
<td>$1,800.00</td>
</tr>
<tr>
<td>Annual SFF Committee Paper Documentation Fee</td>
<td>$300.00</td>
</tr>
<tr>
<td>Annual Surcharge for AIR MAIL to Overseas</td>
<td>$100.00</td>
</tr>
<tr>
<td>Annual Surcharge for Electronic Documentation</td>
<td>$360.00</td>
</tr>
</tbody>
</table>

Name: ____________________________________________
Title: ____________________________________________
Company: __________________________________________
Address: __________________________________________
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Phone: _____________________________
Fax: _____________________________
Email: ____________________________________________

Please register me as a Member of the SFF Committee for one year.

Paper documentation $1,800
Electronic documentation $2,160

Check Payable to SFF Committee for $_____ is Enclosed

Please invoice me $_____ on PO #: _________________

MC/Visa/AmX_____________________________ Expires_____

Please register me as an Observer on the SFF Committee for one year.

Paper documentation $300 U.S. $400 Overseas
Electronic documentation $660 U.S. $760 Overseas

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SFF Committee 408-867-6630
14426 Black Walnut Ct 408-867-2115Fx
Saratoga CA 95070 250-1752@mcimail.com
1. Scope

This specification documents the physical differences between the PCI Card version of the SFP Cage and the original SFP cage design. This variant will broaden the applications of SFP Adapter Modules to PCI Card based systems.

1.1 Description of Clauses

Clause 1 contains the Scope and Purpose.
Clause 2 contains Referenced and Related Standards and SFF Specifications.
Clause 3 contains the General Description
Clause 4 contains the Definitions and Conventions
Clause 5 contains the Cage Descriptions and Dimensions

2. References

The SFF Committee activities support the requirements of the storage industry, and it is involved with several standards.

2.1 Industry Documents

The following interface standards are relevant to this Specification.

- X3.230-1994     FC-PH     Fibre Channel Physical Interface
- X3.297-199x     FC-PH-2  Fibre Channel Physical Interface -2
- X3.303-199x     FC-PH-3  Fibre Channel Physical Interface -3
- IEEE P802.3Z    Gigabit Task Force
- INF-8074i       SFP (Small Formfactor Pluggable) Transceiver

2.2 SFF Specifications

There are several projects active within the SFF Committee. At the date of printing document numbers had been assigned to the following projects. The status of Specifications is dependent on committee activities.

F = Forwarded   The document has been approved by the members for forwarding to a formal standards body.
P = Published   The document has been balloted by members and is available as a published SFF Specification.
A = Approved    The document has been approved by ballot of the members and is in preparation as an SFF Specification.
C = Canceled    The project was canceled, and no Specification was Published.
D = Development The document is under development at SFF.
E = Expired     The document has been published as an SFF Specification, and the members voted against re-publishing it when it came up for annual review.
e = electronic  Used as a suffix to indicate an SFF Specification which has Expired but is still available in electronic form from SFF e.g. a specification has been incorporated into a draft or published standard which is only
The document has no SFF project activity in progress, but it defines features in developing industry standards. The document was provided by a company, editor of an accredited standard in development, or an individual. It is provided for broad review (comments to the author are encouraged).

The document is a proposal to the members for consideration to become an SFF Specification.

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<th>Description</th>
</tr>
</thead>
<tbody>
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<td></td>
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</tr>
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<td>E</td>
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</tr>
<tr>
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<tr>
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</tr>
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</tr>
<tr>
<td>INF-8011i</td>
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</tr>
<tr>
<td>SFF-8012</td>
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</tr>
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</tr>
<tr>
<td>SFF-8014</td>
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</tr>
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</tr>
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</tr>
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</tr>
<tr>
<td>SFF-8019</td>
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</tr>
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</tr>
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<td>SFF-8029</td>
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</tr>
<tr>
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<td>1.8</td>
<td>SFF Committee Charter</td>
</tr>
<tr>
<td>SFF-8031</td>
<td></td>
<td>Named Representatives of SFF Committee Members</td>
</tr>
<tr>
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</tr>
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</tr>
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<td>INF-8035i</td>
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</tr>
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</tr>
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</tr>
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</tr>
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<td>C</td>
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</tr>
<tr>
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</tr>
<tr>
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</tr>
<tr>
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<td>E</td>
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</tr>
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3. General Description

The SFP Cage was initially designed and released as part of a Multi Source Agreement (MSA).

The MSA also included a connector for receiving removeable/pluggable adapter modules. The adapter modules themselves included both copper and optical solutions thus enabling end users the flexibility of implementing solutions based on their particular needs.

It became apparent there was a need to implement SFP's in the server and workstation PCI Card environment. As the initial implementation was geared towards switches where there is no card cage, no provision had been made for them.

This specification defines the additional dimensions required to manufacture an SFP cage which will fit into a PCI card cage.

4. Definitions and Conventions

4.1 Definitions

For the purpose of SFF Specifications, the following definitions apply:

4.1.1 Optional: This term describes features which are not required by the SFF Specification. However, if any feature defined by the SFF Specification is implemented, it shall be done in the same way as defined by the Specification.

4.1.2 Reserved: Where this term is used for bits, bytes, fields and code values; the bits, bytes, fields and code values are set aside for future standardization. The default value shall be zero. The originator is required to define a Reserved field or bit as zero, but the receiver should not check Reserved fields or bits for...
zero.

4.1.3 VU (Vendor Unique): This term is used to describe bits, bytes, fields, pins, signals, code values and features which are not described in this SFF Specification, and may be used in a way that varies between vendors.

4.1.4 VU Mode: A mode of execution by the drive in which its use is not defined by this SFF Specification. The means by which a vendor invokes vendor unique operations within a drive is defined by this SFF Specification.

4.2 Conventions

Certain terms used herein are the proper names of signals. These are printed in uppercase to avoid possible confusion with other uses of the same words; e.g., ATTENTION. Any lower-case uses of these words have the normal American-English meaning.

A number of conditions, commands, sequence parameters, events, English text, states or similar terms are printed with the first letter of each word in uppercase and the rest lower-case; e.g., In, Out, Request Status. Any lower-case uses of these words have the normal American-English meaning.

The American convention of numbering is used i.e., the thousands and higher multiples are separated by a comma and a period is used as the decimal point. This is equivalent to the ISO convention of a space and comma.

<table>
<thead>
<tr>
<th>American</th>
<th>ISO</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.6</td>
<td>0,6</td>
</tr>
<tr>
<td>1,000</td>
<td>1 000</td>
</tr>
<tr>
<td>1,323,462.9</td>
<td>1 323 462,9</td>
</tr>
</tbody>
</table>

4.2 Glossary

PCB: Printed Circuit Board assembly

5. Cage Descriptions and Dimensions

The original SFP cage design as specified in INF-8074i provides the basis for this PCI Card Cage variation. The SFP dimensions specified in INF-8074i are not repeated here, as all of them apply except the one which defines how the cage sits on top of the host PCB. The basic cage is rotated such that it resides on a plane degreed above the mounting surface of the PCB.

Figure 1 provides a view of the cage for a PCI applications and an SFP module.

Figure 2 provides detail of the dimension difference.
Figure 3 defines the dimensions required to locate an opening in the bulkhead to accommodate the SFP PCI Card Cage. The reader's attention is drawn to the fact that these dimensions differ from those defined in INF-8074i and should be used when implementing SFF-8075.